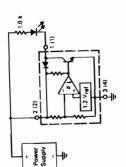
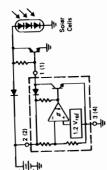
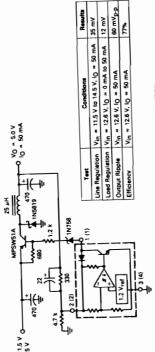
AGURE 10 - VOLTAGE MONITOR



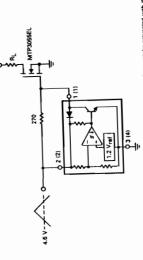
PIGURE 11 — SOLAR POWERED BATTERY CHARGER



PIGURE 12 — LOW POWER SWITCHING REGULATOR



PIGURE 13 — MOSFET LOW-VOLTAGE GATE DRIVE PROTECTION



Overhating of the logic level power MOSET due to insufficient gate voltage can be prevented with the above control. When the input agent is below the 4.6 voil threshold of the MCJ409A, it's output grounds the gate of the UA MOSET.

(M) MOTOROLA

MC34065 MC33065

CURRENT MODE CONTROLLER HIGH PERFORMANCE DUAL CHANNEL

SILICON MONOLITHIC INTEGRATED CIRCUIT

The MC34065 series are high performance, fixed frequency, dual current mode controllers. They are specifically designed for Off-Line and DC to DC converter applications offering the designer a cost effective solution with minimal external components. These integrated circuits feature a uniqua oscillator for precise duty cycle limit and frequency control, a temperature compensated reference, two high gain error amplifiers, two current sensing comparators, drive output 2 enable pin, and two high current totem Also included are protective features consisting of input and cycle current limiting, and a latch for single pulse metering of These devices are available in dual-in-line and surface mount reference undervoltage lockouts each with hysteresis, cycle-by

HIGH PERFORMANCE DUAL CHANNEL

CURRENT MODE CONTROLLER

Advance Information



pole outputs ideally suited for driving power MOSFETs.

PLASTIC PACKAGE CASE 648-08 P SUFFIX

Unique Oscillator for Precise Duty Cycle Limit and Frequency

packages.

 Separate Latching PWMs for Cycle-By-Cycle Current Limiting Internally Trimmed Reference with Undervoltage Lockout

Automatic Feed Forward Compensation

Current Mode Operation to 500 kHz

DW SUFFIX 11
PLASTIC PACKAGE
CASE 751G-01

PIN CONNECTIONS

. P

NO	Package	SO-16	Plastic DIP	80-16	Plastic DIP
ORDERING INFORMATION	Temperature Range	2000	0 to + /U.C.	J. 20 07	7.59+ 01.04-
O. C.	Device	MC34065DW	MC34065P	MC330650W	MC33065P

Direct Interface with Motorola SENSEFET Products

SIMPLIPED BLOCK DIAGRAM

Input Undervoltage Lockout with Hysteresis

 Two High Current Totem Pole Outputs Low Start-Up and Operating Current

Drive Output 2 Enable Pin

18 VCC 19 Ved 19 Ved 19 Votes Fedicies 1 10 Compension 2 11 Commension 2 10 Commension 2 10 Own Out 2 10 Own Out 2 10 Own Out 2	
Sync Incut Cr 2 Cr 2	
S S S	

8	ORDERING INFORMATION	ON
	Temperature	
Device	Range	Package
MC34065DW	Table	50-16
MC34065P	20/+ 010	Plastic DIP
MC330650W	J	80-16
MC33065P	- 40 10 + 62 0	Plastic DIP

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To page

FIGURE 2 -- RESET OUTPUT VOLTAGE VARMA

HGURE 1 — RESET OUTPUT VOLTAGE versus INPUT VOLTAGE

Rt = 10 k to Vin

8 3 3

At = 10 t to Vin

8 2 9

MC34064, MC33064

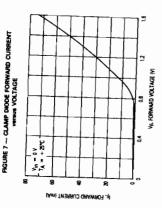


FIGURE 8 — LOW VOLTAGE MICROPROCESSOR RESET

FIGURE 4 — INPUT CURRENT versus INPUT VOLTAGE

HGURE 3 — COMPARATOR THRESHOLD VOLTAGE verus TEMPERATURE

V_{in}, INPUT VOLTAGE IVI

97

6. T

93 7. 32

8

Upper Threshold High State Output

4.810

89 8

R. = 10 k to V_{er}

8 83

Vin. INPUT VOLTAGE (V)

8

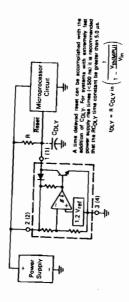
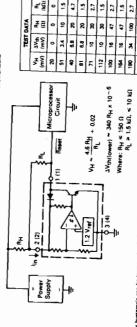


FIGURE 9 — LOW VOLTAGE MICROPROCESSOR RESET WITH ADDITIONAL HYSTERESIS



Comparator hysteresis can be increased with the addition of resistor Ry. The hysteresis equation has been amplied and does not account the bathog of found comment of the sebold (Figure 1), of conseas the comparator threshold (Figure 1), of increase of the lower threshold Shiftigous) will be observed due to IA with its present 340 p.A. at 4.59 V. The equations are accurate to a 10% with Ry less than 150 II and R, between 1.5 435 and 10 M.

THE PARTY

Rt = 10 k Vin = 5.0 V.to 4.0 V TA = 25°C

Reset

TA = + 25°C

Vin = 4.0 V

٦ ع

90%

-40C

TA = +85°C

5 9

VOL. OUTPUT SATURATION (V)

Vin

5.0 V --

4.0 V

3

ğ

FIGURE 6 - RESET DELAY TIME

FIGURE 5 — RESET OUTPUT SATURATION versus SINK CURRENT

V.s. INPUT VOLTAGE (Y)

ş

2

TA, AMBIENT TEMPERATURE (°C)

ڙ آ

85

100 ns/Div.

Sink. SINK CURRENT (mA)

479

478

SENSING CIRCUIT UNDERVOLTAGE

SILICON MONOLITHIC INTEGRATED CIRCUIT

PIN 1. RESET 2. INPUT 3. GROUND PLASTIC PACKAGE CASE 29-04 TO-226AA P SUFFIX

of sinking in excess of 10 mA, and operation is guaranteed down to 1.0 volt input with low standby current. These devices are

packaged in 3-pin TO-226AA and 8-pin surface mount packages. power supply used in appliance, automotive, consumer and

Applications include direct monitoring of the 5.0 volt MPU/logic

Trimmed-in-Package Tempereture Compensated Reference

industrial equipment.

Precise Comparator Thresholds Guaranteed Over

Comperator Threshold of 4.6 V at 25°C

parator with precise thresholds and built-in hysteresis to prevent

designed for use as a reset controller in microprocessor-based systems, It offers the designer an economical solution for low

The MC34064 is an undervoltage sensing circuit specifically voltage detection with a single externel resistor. The MC34064 features a trimmed-in-package bandgap reference, and a comerratic reset operation. The open collector reset output is capable

UNDERVOLTAGE SENSING CIRCUIT

Advance Information

D SUFFIX PLASTIC PACKAGE CASE 751-03

Economical TO-226AA and SO-8 Surface Mount Packages

Internal Clamp Diode for Discharging Delay Capacitor

Guaranteed Reset Operation with 1.0 Volt Input

Low Standby Current

Reset Output Capable of Sinking in Excess of 10 mA

Comparator Hysteresis Prevents Erratic Reset

[empereture]



PIN 1. RESET 2. INPLT 3. N.C. 4. GROUND 5. N.C. 5. N.C. 7. N.C. 8. N.C.

REPRESENTATIVE BLOCK DIAGRAM

	ORDERING	Tempe	1	2	40,	
	ORC	Device	MC34064D-5	MC34064P-5	MC33064D-5	
1.2 Val.		Gnd 0 3 (4) Sink Only	JOSEPH LOGIC	Pin numbers adjacent to terminals are for the 3-pin TO-228AA package.	Fin numbers in perenthesis are for the D suffix SO-8 package.	

_	
4	4
8	\$
ð	Õ

MC34064, MC33064

- 65 to + 150 Internally 0 to + 70 + 150 625 625 stg Sink o š Clamp Diode Forward Current, Pin 1 to 2 (Note 1) Maximum Power Dissipation @ TA = 25°C Thermal Resistance, Junction to Air D Suffix, Plastic Package Maximum Power Dissipation @ TA = 25°C Thermal Resistance Junction to Air Power Dissipation and Thermal Characteristics P Suffix, Plestic Package Reset Output Sink Current (Note 1) Operating Junction Temperature Operating Ambient Temperature MC34064 Storage Temperature Range

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- 1.0 to 10

<u>د</u> ج

Power Input Supply Voltage

Reset Output Voltage

MAXIMUM RATINGS

Symbol

2

siues TA is the operating ambient tempereture	
r typical values TA = 25°C, for min/max values TA	2000
.∢	ž
ECTRICAL CHARACTERISTICS For typical values 1	Charles and anti-

Characteristic	Symbol	Ali.	g,	Mex	Unit
COMPARATOR					
Threshold Voltage					>
High State Output (Vin Increasing)	¥	4.5	4.61	4.7	
Low State Output (Vin Decreasing)	۷,	4.5	4.59	4.7	
Hysteresis	7	0.01	0.02	90.0	
RESET OUTPUT					

1 6 6 6 (13) 6 9		ΙΟΛ	1	A) - 0.15	(Nin = 1.0 V, ISink = 0.1 mA)	Output Sink Current (Vin. Reset = 4.0 V)	Output Off-State Leakage (Vin. Reset = 5.0 V)	Clemp Diode Forward Voltage, Pin 1 to 2 (Ir = 10 mA)
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TOTAL DEVICE					
Operating Input Voltage Range	Vin	1.0 to 6.5	!	1	>
Quiescent Input Current (Vin = 5.0 V)	Ę	-	390	200	4
NOTES:					

Maximum pectage power dissipation limits must be observed.
 Low duty deep buils schniques are used during test to mainten indicion temperature as close to ambient as possible.
 Tow a. O'C for Michaels
 Tow a. - 4°C for Michaels
 - - 4°C for Michaels
 - - 4°C for Michaels

MC33084D-5 -40°C to +85°C Pleetic SO-8 MC33084P-5 -40°C to +85°C Pleetic TO-228AA Plastic TO-226AA Plastic SO-8 Package INFORMATION STATE

477

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